GAU-2631



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Application / Conf. No.	09/665,884
Filing Date	September 20, 2000
First Named Inventor	Christopher H. Dick
Examiner Name	Not Yet Known
Group Art Unit	2631
Issue Fee Batch No.	
Attorney Docket Number	X-501-1P US

Express Mail Receipt No.		Attorney Docket Number	X-501-1P US				
ENCLOSURES (check all that apply)							
Fee Transmittal Form		nment Papers Recordation Cover Sheet)	After Allowance Communication to				
Amendment / Response Preliminary Amendment After Final Affidavit(s)/declaration(s) Extension of Time Request Change Status to LARGE ENTITY	Drawin	ing-related Papers	Appeal Communication to Board of Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) Status Letter X Return Receipt Postcard				
Express Abandonment Request	Provis	nvert a ional Application	Additional Enclosure(s) (please identify below):				
X Information Disclosure Statement X Substitute PTO-1449(s) IDS by Applicant (PTO/SB/08A) Certified Copy of Priority	Power of Attorney, Revocation Change of Correspondence Address Terminal Disclaimer Request for Refund		Copies of fourteen (14) references cited. RECEIVED				
Document(s) Response to Missing Parts/ Incomplete Application Response to Missing Parts under 37 CFR 1.52 or 1.53	Remarks		MAY 1 5 2001 Technology Center 2600				
SIGNATURE	OF APPL	ICANT, ATTORNEY, O	RAGENT				
Customer Number Attn: Edel M. Young		24309 (Customer Number)	Reg. Number 32,451				
Date May 9, 2001 Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040							
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IN THE UNITED STATES PATENT OFFICE

Applicants:

Christopher H. Dick and Frederic J. Harris

Assignee:

Xilinx, Inc.

Title:

"Tunable Narrow-Band Filter Including Sigma-Delta

Modulator"

MAY 1 1 2001

grial No.:

09/665,884

File Date: 09/20/00

xaminer:

Unknown

Art Unit: 2631

Docket No.:

X-501-1P US

COMMISSIONER FOR PATENTS Washington, D. C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the fourteen (14) references listed in the attached Form PTO-1449. A copy of each is enclosed herein. This Information Disclosure Statement is being submitted prior to receipt of a first office action in the above application.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully Submitted,

Edel M. Young

Agent for Applicants

Reg. No. 32,451

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Mary O'Malley

Name EMY : mom Signature

MAY 1 1 2001

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Substitute for form 1449B/PTO				Complete if Known			
_					Application Number	09/665,884 /	
	INFO	RMATION	DISC	LOSURE	Filing Date	September 20, 2000	
7		TEMENT E	BY AP	PLICANT	First Named Inventor	Christopher H. Dick	
Ş	A .				Group Art Unit	2631	
-	(use as many sheets as necessary)			ssary)	Examiner Name	Not Yet Known	
Ş	5heet	1	of	2	Attorney Docket Number	X-501-1P US	

y			
		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
Examiner Initials *	Cite No¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		Hwang, Kai, "Computer Arithmetic Principles, Architecture, and Design"; Copyright 1979 by John Wiley & Sons, Inc.; pages 152-155.	
		Marvin E. Frerking, "Digital Signal Processing in Communication Systems", published by Van Nostrand Reinhold, New York, 1994, pp. 124-128 and 171-174.	
		John R. Deller, Jr. et al., "Discrete Time Processing of Speech Signals", published by Macmillan Publishing Company, Maxwell Macmillan International, New York, 1993, pp. 273-280.	
		"The Programmable Logic Data Book", 1999, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124, pp. 6-5 to 6-41.	
		L. Mintzer, "FIR Filters with Field-Programmable Gate Arrays", Journal of VLSI Signal Processing, No. 6, 1993, pp. 119-127.	Techn
		S. Mohanakrishnan et al., "Automatic Implementation of FIR Filters on Field Programmable Gate Arrays", IEEE Signal Processing Letters, Vol. 2, No. 3, March 1995, pp. 51-53.	Technology Ceriter 2800
		D. Patterson et al., "Computer Architecture: A Quantitative Approach", Morgan Kaufmann Publishers, Inc., California, 1990, pp. A8-A10.	Her 2600
ti		C.H.Dick and F.J. Harris; "FPGA Signal Processing Using Sigma-Delta Modulation"; IEEE Signal Processing Magazine, January 2000	
		Atmel AT40K/05/10/20/40 Data Sheet, 1999	
1c		A. Peled and B. Liu, "A New Hardware Realization of Digital Filters" IEEE Trans. on Acoust., Speech, Signal Processing, Vol. 22, pp. 456-462, December 1974.	
-2	-	S.A. White, "Applications of Distributed Arithmetic to Digital Signal Processing" IEEE ASSP Magazine, Vol. 6(3), pp. 4-19, July 1989	

Examiner Signature		Date Considered	
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Substitute for form 1449B/PTO		Complete if Known			
INFORMATION DISCLOSURE				Application Number	09/665,884 /
				Filing Date	September 20, 2000
STATEMENT BY APPLICANT			PPLICANT	First Named Inventor	Christopher H. Dick
•				Group Art Unit	2631
	(use as many sheets as necessary)			Examiner Name	Not Yet Known
Sheet	2	of	2	Attorney Docket Number	X-501-1P US

	OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No '	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T 2			
×	•	Application Note AN10, entitled "Delta Sigma A/D Conversion Technique Overview" January 1997, available from Crystal Semiconductor Corporation, P.O. Box 17847, Austin TX 78760, http://www.crystal.com, pp. 1-10.				
AS		Multidimensional autoregression Chapter 6, pp. 135-169				
*		C.H. Dick and F. Harris, "FIR Filtering with FPGAs Using Quadrature Sigma-Delta Modulation Encoding", 6th Int. Workshop on Field-Programmable Logic and Applications, FPL September 1996, Darmstadt, Germany				
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Examir Signatu	ner	Date Considered				

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